NRI
Established by:
Date:

Customer
Customer No.:
Name/address:
Order No.:

Order

	Order quantity	Country
Order date:		
Desired delivery date:	Special requirements:	
Final date		

Device

Device type, if kown	
Configuration, if known	
Version, if known	
Data block, if known	
Type of machine/interface	

Model-No.

Hardware

	Standard	with MIDI front plate	with MINI front plate	for front plate	Special
ECV	☐ 06V	☐ 07V	☐ 08V	☐ 09V	☐ 10V
Totalizer	☐ 06T	☐ 07T	☐ 08T	☐ 09T	☐ 10T
Bus	☐ 06B	☐ 07B	☐ 08B	☐ 09B	☐ 10B
Casino	☐ 06C				☐ 10C

NOTE: See General Interface
information on page 3
for Model Type

Connecting cable:	Length 400 mm ☐
Voltage 12 V DC	☐

Setting

Length of coin pulse: standard: YES ☐   NO ☐

other: 15 mS ☐   50 mS ☐

Other Specify: (mS) _____

Standard coin length pulse is 100mS (50 on/50 off)

For totalizer

2-price	TV	Timer
☐	☐	☐

2 Price External reset required

TV No exteranal reset required (Automatic)

Timer No reset pulse required

Position of mounting studs:
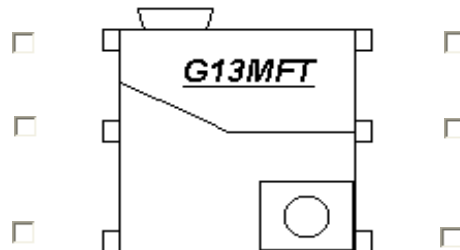
Order Form G-13 MFT

Coins

Channel	Currency (Country)	Value (Denomination)	Sorting (no,R,1,2,3)	Output coin pulse (1,2,3,4,5,6)	Number (1...255)	blocking switch at ECV	External single inhibit(1,2,3,4,5,6,-)	Internal single inhibit (1,2,3,4,5,6,-)

Block 0

Channel	Currency (Country)	Value (Denomination)	Sorting (no,R,1,2,3)	Output coin pulse (1,2,3,4,5,6)	Number (1...255)	blocking switch at ECV	External single inhibit(1,2,3,4,5,6,-)	Internal single inhibit (1,2,3,4,5,6,-)
01								
02								
03								
04								
05								
06								
07								
08								
09	TK	TK		6	1			
10	TK	TK		6	1			
11	TK	TK		6	1			
12	TK	TK		6	1			
13	TK	TK		6	1			
14	TK	TK		6	1			
15	TK	TK		6	1			
16	TK	TK		6	1			

Block 1

Channel	Currency (Country)	Value (Denomination)	Sorting (no,R,1,2,3)	Output coin pulse (1,2,3,4,5,6)	Number (1...255)	blocking switch at ECV	External single inhibit(1,2,3,4,5,6,-)	Internal single inhibit (1,2,3,4,5,6,-)
01/17								
02/18								
03/19								
04/20								
05/21								
06/22								
07/23								
08/24								
09/25	TK	TK		6	1			
10/26	TK	TK		6	1			
11/27	TK	TK		6	1			
12/28	TK	TK		6	1			
13/29	TK	TK		6	1			
14/30	TK	TK		6	1			
15/31	TK	TK		6	1			
16/32	TK	TK		6	1			

Text:

Explantion of Column Headings

Channel: This is the memory block within the ECV into which a specific coins programming information is stored. NOTE: If Medium or high security channels are programmed the same coin will be programmed on more than one channel I.E. Canadian $1.00 Standard security on Channel 1, High security $1.00 Canadian coin on channel 4.

Currency (Country): This would be the country for which the currency programmed would be programmed I.E. Canada, US, Euro. (Multiple countries may be programmed on one validator.

Value (denomination): Coin value or type of token

Sorting (no, R ,1 ,2, 3): If sorting required which sort output to be made active.

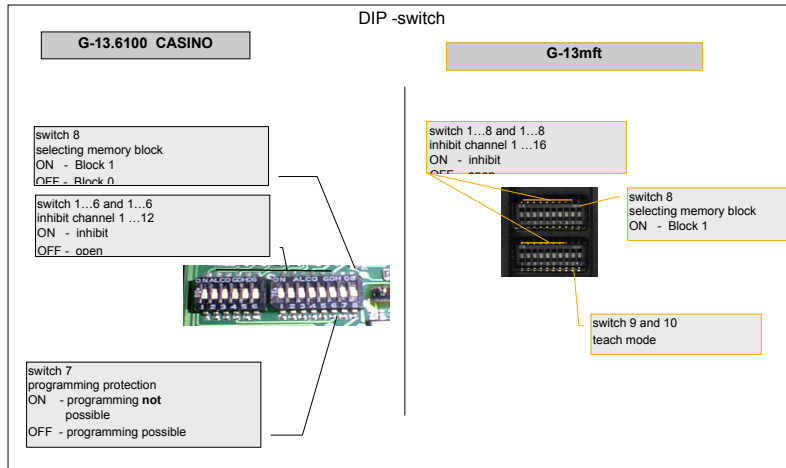**Number (1…255):** If multipulse number of pulses required for that coin.

G-13.6100 CASINO

Connector	10 pin	assignment	specification
PIN 1	GND		
PIN 2	UB +12V		Imax 30mA + approx. 3W acceptence magnet
PIN 3	output line 5	act. low	typ. 100ms* (opt. 30-300ms)
	output line 6	act. low	typ. 100ms* (opt. 30-300ms)
PIN 4	accept sensor signal for CASINO version	act. low	the signal is a direct consequence of the CP3 opto beam being intercepted by the coin
PIN 5			
PIN 6	total blocking	act. high	machine inhibits coin acceptance
PIN 7	output line 1	act. low	typ. 100ms* (opt. 30-300ms)
PIN 8	output line 2	act. low	typ. 100ms* (opt. 30-300ms)
PIN 9	output line 3	act. low	typ. 100ms* (opt. 30-300ms)
PIN 10	output line 4	act. low	typ. 100ms* (opt. 30-300ms)
			*minimum time between two signals is 10ms
	Acceptance rate	4	coin in per sec.

G-13mft06C CASINO

Connector	10 pin	assignment	specification
	GND		
	UB +12V		Imax 100mA + approx. 3W acceptence magnet
	output line 5	act. low	typ. 100ms* (opt.10-500ms)
	output line 6	act. low	typ. 100ms* (opt.10-500ms)
	accept sensor signal for CASINO version	act. low	the signal is a direct consequence of the CP3 opto beam being intercepted by the coin
	total blocking	act. high	machine inhibits coin acceptance
	output line 1	act. low	typ. 100ms* (opt.10-500ms)
	output line 2	act. low	typ. 100ms* (opt.10-500ms)
	output line 3	act. low	typ. 100ms* (opt.10-500ms)
	output line 4	act. low	typ. 100ms* (opt.10-500ms)
			*minimum time between two signals is 10ms
	Acceptance rate	4	coin in per sec.

G-13mft06V Standard

10 pin	assignment	specification
GND		
UB +12V		Imax 100mA + approx. 3W acceptence magnet
output line 5	act. low	typ. 100ms* (opt.10-500ms)
output line 6	act. low	typ. 100ms* (opt.10-500ms)
return	act. low	after the return button has been pressed, the validator signals the opening of the measurement area
total blocking	act. high	machine inhibits coin acceptance
output line 1	act. low	typ. 100ms* (opt.10-500ms)
output line 2	act. low	typ. 100ms* (opt.10-500ms)
output line 3	act. low	typ. 100ms* (opt.10-500ms)
output line 4	act. low	typ. 100ms* (opt.10-500ms)
		*minimum time between two signals is 10ms
Acceptance rate	2	coin in per sec.

G-13mft96C SGI 100

10 pin	assignment	specification
GND		
UB +12V		Imax 100mA + approx. 3W acceptence magnet
output line 5	act. low	typ. 17ms* (opt. 10-500ms)**
sence	act. low	Coin validator signals valid coin (measurement system passed), also with "Inhibit" **
tilt	act. low	Error - pulse see "Error diagnosis" ***
total blocking	act. high	machine inhibits coin acceptance
output line 1	act. low	typ. 17ms* (opt. 10-500ms)**
output line 2	act. low	typ. 17ms* (opt. 10-500ms)**
output line 3	act. low	typ. 17ms* (opt. 10-500ms)**
output line 4	act. low	typ. 17ms* (opt. 10-500ms)**
		*minimum time between two signals is 10ms
Acceptance rate	4	coin in per sec.

G-13mft96C SGI 62

Connector	7 pin	assignment	specification
PIN 1	GND		
PIN 2	sence		Coin validator signals valid coin (measurement system passed), also with "Inhibit" **
PIN 3	tilt	act. low	Error - pulse see "Error diagnosis" ***
PIN 4	output line	act. low	typ. 17ms* (opt. 10-500ms)**
PIN 5	-		
PIN 6	UB +12V		Imax 100mA + approx. 3W acceptence magnet
PIN 7	total blocking	act. High (10K to 5V)	machine inhibits coin acceptance
			*minimum time between two signals is 10ms
	Acceptance rate	4	coin in per sec.

DIP -switch

G-13.6100 CASINO

switch 8
selecting memory block
ON - Block 1
OFF - Block 0

switch 1…6 and 1…6
inhibit channel 1 …12
ON - inhibit
OFF - open

switch 7
programming protection
ON - programming **not** possible
OFF - programming possible

G-13mft

switch 1…8 and 1…8
inhibit channel 1 …16
ON - inhibit
OFF - open

switch 8
selecting memory block
ON - Block 1

switch 9 and 10
teach mode

Output – Timing

Sense

Credit

Coin 1 (2 pulses)   Coin 2 (4 pulses)
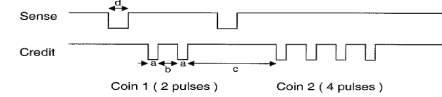
name	possible range of values	typical value	specification
a	Coin impulse length	10 … 500 ms	17 ms	
b	Multipulse pause	10 … 255 ms	as coin impulse length	pause between the pulses from one coin
c	Coin pause	10 … 255 ms	as coin impulse length	Minimum pause between the pulses from different coins
d	Sense impulse length	10 . 255 ms	as coin impulse length	

Error diagnosis

Error cause	Impulse length
Supply voltage too high (~18 V)/ too low (~8 V)	Error duration
Checksum error (device defective)	Error duration
Coin pile-up	Error duration
Sensor error (covered/dirty/defective)	Error duration
String recognition (coin acceptance inhibited for 30 sec.)	30 sec.
Inhibited coin accepted (acceptace gate permanently open)	Coin impulse length (is outputted together with coin signal)
Non-porgrammed coin accepted (acceptace gate permanently open)	Coin impulse length
Coin too slow, could not be measured	Coin impulse length

NRI G-13.mft SGI Interface Variants

G-13.mft term	SGI 16 A	SGI 16 B	SGI 40 A	SGI 46 A	SGI 62 A	SGI 62 AS (Single Coin)	SGI 33 A	SGI 80 A	SGI 100 A
Connector type	JST	JST	JST	Molex	Molex	Molex	JST	JST	10-pole terminal strip
Pin assignment									
Pin 1	Inhibit (active high) 1K to GND	Inhibit (active high) 10K to 5 V	Tilt (active low)	Inhibit (active high) 10K to 5 V	GND	GND	12 V DC*	GND	GND
Pin 2	Sense (active low)	Sense (active low)	Inhibit (active high) 10K to 5 V	Tilt (active low)	Sense (active low)	Sense (active low)	Sense (active low)	Inhibit (active high) 10K to 5 V	12 V DC
Pin 3	nc*	nc	Credit (active low)	Credit (active low)	Tilt (active low)	Tilt (active low)	GND	Ready (active low)	Credit 5 (active low)
Pin 4	nc*	nc*	nc*	Sense (active low)	Credit (active low)	Credit (active low)		Tilt (active low)	Sense (active low)
Pin 5	12 V DC	12 V DC	12 V DC	12 V DC	nc	nc		Credit (active low)	Tilt (active low)
Pin 6	GND	GND	GND	GND	12 V DC	12 V DC		Sense (active low)	Inhibit (active high) 10K to GND
Pin 7					Inhibit (active high) 10K to 5 V	Inhibit (active high) 10K to 5 V		12 V DC	Credit 1 (active low)
Pin 8								nc	Credit 2 (active low)
Pin 9									Credit 3 (active low)
Pin 10									Credit 4 (active low)
Compatible with	CC16	CC16	CC40	CC46	CC62	MC40	CC33	Defender 3	–

* Deviation from Coin Comparitor

We reserve the right for technical changes !

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